

PATENT ABSTRACTS OF JAPAN

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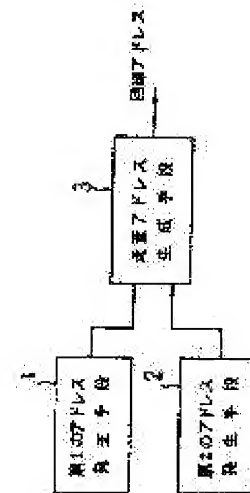
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(54) LINE SCANNING SYSTEM

(57)Abstract:

PURPOSE: To distribute a prescribed scanning capability or over to all lines regardless of any scan control to accommodate a high speed circuit by scanning a line having a line address generated by a scanning address generating means sequentially.

CONSTITUTION: The scanning address generating means 3 generates a line address to allow a line scanning mechanism to scan by outputting selectively a line address outputted from the 1st and 2nd address generating means 1, 2 alternately. The line scanning mechanism scans sequentially a line having a line address generated by the scanning address generating means 3. Thus, a prescribed value or over the scanning capability is distributed to all the lines independently of the scan control implemented to accommodate a high speed line.



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